# Efficient 2.45 GHz Rectenna Design with high Harmonic Rejection for Wireless Power Transmission

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#### Abstract

The purpose of this work is to propose an efficient microstrip rectenna operating on ISM band with high harmonic rejection. The receiving antenna with proximity coupled feeding line implemented in a multilayer substrate. The rectenna with integrated circular sector antenna can eliminate the need for an low pass filter (LPF) placed between the antenna and the diode as well as produce higher output power, with maximum conversion efficiency of 74% using a 1300  $\Omega$  load resistor at a power density of 0.3 mW/cm<sup>2</sup>.

*Keywords:* WPT, Rectenna, Rectifying circuit, Proximity coupled antenna, Defect ground structure.

# 1. Introduction

Rectifying antenna (rectenna) which can convert RF energy to DC power plays an important role in free space wireless power transmission (WPT). Over the last century, the development of rectenna for space solar power transmission (SSPT) [1] as well as WPT [2] had great achievement with specific functions; and the applications e.g., actuator [3] or wireless sensors [4].

The typical rectenna in the prior literatures [5]–[7] basically consists of four elements: antenna, low pass filter (LPF), diodes, and DC pass capacitor. The initial development of rectenna focuses on its directivity and efficiency for great power reception and conversion, hence, large array [8] was usually adopted for microwave power reception. Afterward, many functions were added to enhance the performance of the rectenna array, such as arbitrary polarization [9], dual-polarization [10], CP [11], and dual band [12]. Besides, for the antenna integrated with nonlinear circuits, such as diodes and FETs, it is well known that harmonics of the fundamental frequency would be generated. The unwanted harmonics cause problems of harmonics re-radiation and efficiency reduction of rectenna; then the LPF is required to suppress harmonics to improve system performance and prevent harmonics interference. For size reduction and cost down, the antenna with harmonic rejection property was proposed to eliminate LPF [13]. In this paper, a microstrip rectenna with harmonic rejection property is proposed it shown in figure 1.

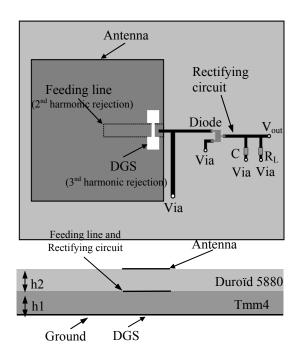


Figure 1. Proposed rectenna with harmonic rejection

#### 2. Antenna design

The rectangular radiating patch is printed on the side of the first substrate while the microstrip feed line is on the upper side of the second substrate, the ground plane, and the dumbbell shape slot, are on lower side of the second substrate. The relative permittivities and the thickness are  $\varepsilon_{r1}$ =2.2,  $\varepsilon_{r2}$ =4.5, h<sub>1</sub>=1.575 mm and h<sub>2</sub>=1.524 mm. We should emphasize that the value of  $\varepsilon_{r1}$  has been chosen small to enhance the patch radiation. Similarly, the

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quantity  $\varepsilon_{r2}$  has been chosen so high to reduce the size of microstrip feed line compared to the radiating element. The line is dimensioned for 50 $\Omega$  characteristic impedance and a bandwidth of 2.845 mm.

The length and width of the patch are 34.2 mm and 34.9 mm respectively, which are dimensioned to resonate at 2.45GHz frequency. The slot in the ground plane, whose dimensions are given on the Figure 2. The electromagnetic energy is coupled from the microstrip feed line to the patch, the slot avoid a frequency stop band.

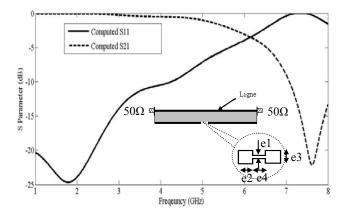


Fig. 1 S-parameters and side view of the DGS used to reduce the third harmonic in the proposed design, e1=0.255mm, e2= 2. 95mm, e3=2.1mm, e4=2.84mm

The use of a DGS allows the apparition of a stop band controlled by tuning the dimensions of the slot. The S parameter result of the DGS is shown in figure 2.

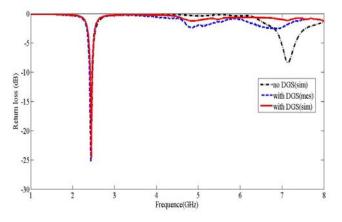


Fig. 2 Simulated and Measured reflection coefficient versus frequency.

The measured return loss impedance bandwidth is about 2.6% for a 6.4 dB gain at 2.45GHz operating frequency. It can be noted that the mismatches at the harmonics frequencies 4.9GHz and 7.35GHz are -1.95 dB and -1.75 dB respectively.

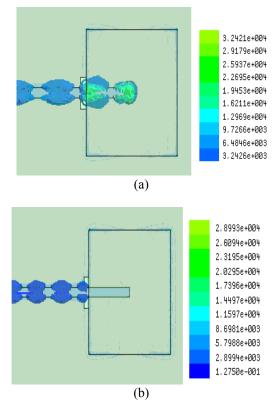


Fig. 3 Field distribution (V/m) at 4.9 GHz (a) and 7.35 GHz (b).

Figure 3 shows the current distribution at 4.9 GHz and 7.35 GHz

#### 3. Rectenna measurements

The receiving antenna and rectifying are connected by SMA connectors as shown in Fig. 4. It contains a linearly polarised patch antenna designed at 2.45 GHz by using HFSS software[14]. The rectenna contains one HSMS2860 commercial Schottky diodes in a SOT23 package. The zero bias junction capacitance Cj0 is 0.18 pF and the series resistance Rs is 5 V.

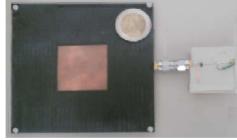


Fig. 4 Photograph of the proposed rectenna

The experiments have been carried out in anechoic chamber. The transmitting antenna is a standard linear polarized horn with gain Gt of 12 dB. The rectenna is located at the distance r of 70 cm, which is the far region



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of the horn. The output DC voltage and overall efficiency have been measured against power density from the Friss transmission equation(1).

$$P_r = \left(\frac{\lambda}{4\pi r}\right)^2 P_t G_t G_r \tag{1}$$

where Pt is the transmitting power; Gr is the receiving antenna gain; is the free space wavelength at 2.45 GHz. So the RF-DC conversion efficiency is calculated by formula (2).

$$\eta_{\rm r} = \frac{\frac{(V_{\rm out})^2}{R_{\rm L}}}{P_{\rm r}}$$
(2)

The rectenna is illuminated by a linearly polarised incident plane wave of 20 V/m  $(0.10 \text{ mW/cm}^2)$  at its broadside. On the transmitter side, we have used a 30 dB gain power amplifier at 2.45 GHz connected to a signal generator. The output DC voltage across the resistor load has been measured by a voltmeter.

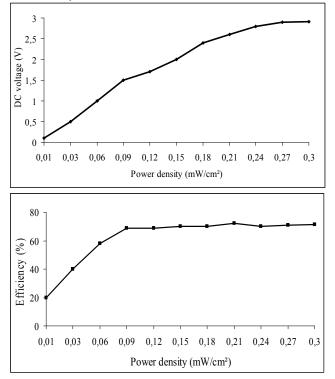


Fig. 5 Measured DC voltages and rectenna efficiency against power density

The measured output DC voltages and overall efficiency are shown in Fig. 5. In the power density range  $(0-0.3 \text{ mW/cm}^2)$ , the measured rectenna efficiency is above 74% from 0.3 mW/cm<sup>2</sup> power density and the corresponding

output DC voltage is 2.9 V over a 1300  $\Omega$  optimised load resistance. The measured results show that

the output voltage and efficiency increase when the power density increases.

In applications, the antenna and rectifying circuit can be integrated directly on one substrate by omitting SMA connectors. Without the loss of SMAs, the efficiency would be higher.

# 4. Conclusion

An efficient rectenna design, based on a series diode circuit topology is proposed. An optimised length of the feeding line and defect groud structure are used to reject the second and the third harmonics. No input low pass filter is needed, thus reducing the insertion losses and the dimensions of the circuit. The rectifying circuit has been optimised at 2.45 GHz for an input power of 10 dBm. The rectenna exhibits a measured efficiency of 74 % at 0.3 mW/cm<sup>2</sup> power density and an output DC voltage of 2.9 V.

## 5. Acknowledgment

We thank D. Delcroix and the Esycom laboratory of University of Marne-La-Vallée in Paris.

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