Frequency Domain Soft-Decision Feedback Equalization for SC-FDMA with Insufficient Cyclic Prefix

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Abstract

Single-carrier frequency-division multiple access (SC-FDMA) system suffers performance degradation when the length of the cyclic prefix (CP) is less than the channel delay spread. In this paper, based on MMSE criterion, we proposed an efficient frequency domain soft-decision feedback equalization (FD-SDFE) scheme for SC-FDMA system. The proposed scheme employs residual inter-symbols cancellation (RISIC) algorithm, combining a priori information to reduce inter-block interference (IBI) and inter-carrier interference (ICI) component caused by the absence of CP in multipath fading channel. In addition, an FD-SDFE is derived to further mitigate the residual interference. Simulation results show that the proposed scheme has a significant performance improvement.

Keywords: SC-FDMA, Frequency domain (FD), soft-decision feedback equalization (SDFE), cyclic prefix (CP)

1. Introduction

Single-carrier frequency-division multiple access (SC-FDMA) has been adopted as the uplink transmission in Third-Generation Partnership Project Long-Term Evolution (3GPP-LTE), due to its low peak-to-average power ratio (PAPR) and high frequency diversity gain [1], [2]. SC-FDMA is a combination of FDMA and single-carrier modulation with frequency domain equalization (SC-FDE), which has similar implementation complexity and bit error rate (BER) performance, compared with orthogonal frequency-division multiple access (OFDMA) [3]. SC-FDMA should have a cyclic prefix (CP) long enough to cope with the maximum channel delay spread, thus, the inter-block interference (IBI) is avoided, at the same time, the linear convolution in the time domain can be converted into circular convolution in the frequency domain. However, the use of CP reduces the spectral efficiency of SC-FDMA.

In recent years, several approaches have been proposed to overcome the performance degradation due to the insufficient CP, such as residual inter-symbol interference cancellation (RISIC) [4], Turbo FDE technique [5]. In [5], Turbo equalization scheme achieves performance improvement by iteratively exchanging the soft extrinsic information between detector and soft-input soft-output (SISO) decoder. [6], [7]

In this paper, we propose a new, non-linear, low-complexity MMSE-based frequency domain soft-decision feedback equalization (FD-SDFE), a turbo equalization scheme for SC-FDMA with insufficient CP, which shares some similarities with soft-feedback equalizer (SFE) in [8], [9], but unlike [8], we implement the feedforward filter and feedback filter both in frequency domain, thus computational complexity is reduced. In our scheme, we first use previously detected soft symbols to reduce IBI and ICI as in [4], [10], and then FD-SDFE is performed to cancel residual interference. Moreover, equalizer coefficients are dynamically formulated with the help of the a priori mean and the variance of the symbols, thus, the cross-correlation function between the detected symbols and the transmitted symbols in [5] is avoided. Simulation results show that the proposed scheme can achieve good performance.

The remainder of the paper is organized as follows. The signal model and interference analysis of SC-FDMA is described in Section II. Section III derives the FD-SDFE turbo equalization. Error performance and computational complexity are presented in Section IV. Finally, the conclusions are drawn in Section V.

Notations: In this paper, capital letters denote entities in the frequency domain and lowercase letters denote entities in the time domain. Bold symbols denote matrices or
vector. \( A^{-1}, A^T, A^H \) denote the inverse, transpose, and conjugate transpose of matrix \( A \), respectively. \( \mathbb{C}^{M \times N} \) denotes the space of all \( M \times N \) complex matrices and \( I_N \) is the \( N \times N \) identity matrix. The operator \( \text{diag} \{ \cdot \} \) to be applied to a length \( N \) vector denotes an \( N \times N \) squared matrix with the vector elements along the diagonal. \( F_N \) is the normalized \( N \times N \) DFT matrix with its entry \( F_{pq} = \frac{1}{\sqrt{N}} \exp(-j2\pi pq/N) \) for \( p, q = 0, 1, \ldots, N-1 \).

Finally, \( E \{ \cdot \} \) denotes the expectation operation and \( \text{tr} \{ \cdot \} \) represents the trace of a matrix.

2. SYSTEM MODEL AND INTERFERENCE ANALYSIS

We consider a SC-FDMA system with \( U \) users that may transmit simultaneously. Each user is equipped with single transmit antenna and the base station has single receive antenna. Assume that there are totally \( N \) subcarriers, the number of subcarriers for each user is \( M = N/U, N \gg M \).

![Diagram](image)

**Fig. 1.** The transmitter structure of SC-FDMA

### 2.1 SC-FDMA Signal

The structure of the SC-FDMA is depicted in Fig. 1. We assume perfect time synchronization. As illustrated in Fig. 1, at the communication transmitter, the binary information, \( b_n \in \{0, 1\} \), is passed through channel encoder followed by an interleaver. The output of the interleaver is divided into blocks with length \( K \cdot M \), where \( K = \log_2 Q \), \( Q \) is the modulation level. The output of the interleaver can be denoted in vector from as \( \mathbf{c} = [c_1, c_2, \ldots, c_U]^T \in \mathbb{C}^{KM \times 1} \), where \( c_k = [c_{k,1}, c_{k,2}, \ldots, c_{k,K}] \in \mathbb{C}^{K \times 1} \). The symbol mapper maps \( K \)-bit data, \( c_n \), to one modulation symbol, \( x_n \in S \), where \( S = \{\alpha_1, \alpha_2, \ldots, \alpha_S\} \) is the modulation constellation set with \( \alpha_i \in \mathbb{C} \), then grouped into block of the transmission. Without loss of generality, each block of symbols can be expressed as \( \mathbf{x} = [x(0), x(1), \ldots, x(M-1)]^T \). After that, \( \mathbf{x} \) is transformed to frequency domain signal \( \mathbf{X} \) by a normalized \( M \) -point DFT. Through corresponding subcarrier mapping scheme, we can get \( N \) -point frequency domain signal \( \mathbf{s} = [S(0), S(1), \ldots, S(N-1)]^T \), subsequently, time domain data block \( s = [s(0), s(1), \ldots, s(N-1)]^T \) is obtained by a normalized \( N \) -point inverse DFT (IDFT).

After \( G \) symbols are inserting as CP in the front of \( s \), the SC-FDMA signal block is transmitted over the multipath fading channel. The length of the multipath channel is defined as \( L \), and it is assume that \( L-1 \geq G \).

### 2.2 Interference Analysis

At the receiver, the received time domain signal after removing CP can be can be represented in matrix format as

\[
\mathbf{r}_i = \mathbf{H}_b \mathbf{s}_i + \mathbf{H}_i \mathbf{s}_{i-1} + \mathbf{v}_i
\]

where

\[
\mathbf{r}_i = [r_{i,0}, r_{i,1}, \ldots, r_{i,N-1}]^T \in \mathbb{C}^{N \times 1},
\]

\[
\mathbf{v}_i = [v_{i,0}, v_{i,1}, \ldots, v_{i,N-1}]^T \in \mathbb{C}^{N \times 1}
\]

are the \( i \) th block receive data and noise vector with variance \( \sigma^2 \mathbf{I}_N \), respectively. \( \mathbf{H}_b \) denotes lower triangular matrix of \( \mathbb{C}^{MN \times MN} \) and \( \mathbf{H}_i \) is upper triangular matrix of \( \mathbb{C}^{MN \times MN} \).

\[
\mathbf{H}_b = \begin{bmatrix}
0 & 0 & \cdots & 0 \\
0 & 0 & \cdots & 0 \\
0 & 0 & \cdots & 0 \\
0 & 0 & \cdots & 0
\end{bmatrix} \in \mathbb{C}^{N \times N}
\]

\[
\mathbf{H}_i = \begin{bmatrix}
0 & 0 & \cdots & h_{i-1} & \cdots & h_{i-1} \\
0 & 0 & \cdots & 0 & \cdots & 0 \\
0 & 0 & \cdots & 0 & \cdots & 0 \\
\vdots & \vdots & \ddots & \vdots & \ddots & \vdots \\
0 & 0 & \cdots & 0 & \cdots & 0
\end{bmatrix} \in \mathbb{C}^{N \times N}
\]

where \( h \) is the time domain channel CIR. Since circulant Toeplitz matrix \( \mathbf{H}_i \) is the sum of matrices \( \mathbf{H}_b \) and \( \mathbf{H}_i \), we can rewrite (1) as follows

\[
\mathbf{r}_i = \mathbf{H}_b \mathbf{s}_i + \mathbf{H}_i \mathbf{s}_{i-1} + \mathbf{v}_i
\]

where \( \mathbf{H}_b \mathbf{s}_i \) denotes the IBI component caused by the previous block, and \( \mathbf{H}_i \mathbf{s}_{i-1} \) represents the ICI component.

3. FD-SDFE DESIGN

For SC-FDMA system, equalizer design is essential to
The vector is fed into FD-\(\hat{M}\) and is the user assigned \(i\). The result \(I(0)\) and \(I(1)\) can be obtained \(I(7)\) and \(I(8)\) is frequency \((5)\) from is to remove the IBI term by \(\hat{X}(\hat{e})\) are the feedforward and \(-\)point IDFT. \(1\) M. It is also assume \(\hat{S}\) in (4), or equivalently to perform soft CP reconstruction. Under the \(\hat{r}_i\) is first \(\hat{r}_i\) and repeat step 3)~5) until convergence occurs or maximum number of iterations is reached.

**3.2 FD-SDFE Turbo Equalization**

In this part, we consider the FD-SDFE turbo equalization scheme in SC-FDMA system. The coefficients of the feedforward and feedback filters are obtained by minimizing the mean square error (MSE).

We concentrate on the receiver structure in Fig.2, the proposed FD-SDFE turbo equalization consists of a feedforward linear equalizer and feedback filters both in the frequency domain. For the convenience of derivation of the optimum tap coefficient of FD-SDFE turbo equalization, we ignore the block index \(i\). It is also assume that ICI component has been mitigated completely.

The FD-SDFE outputs \(\hat{R}\) from \(\hat{R}\) and \(\hat{X}\), can be expressed as
\[
\hat{R} = W\hat{R} - B\hat{X}
\]
where \(W = diag\{W_0, W_1, ..., W_{M-1}\} \in \mathbb{C}^{M \times M}\) and \(B = diag\{B_0, B_1, ..., B_{M-1}\} \in \mathbb{C}^{M \times M}\) are the feedforward and feedback filter coefficient, respectively. \(\hat{R}\) is de-mapped frequency domain signal from \(\hat{R}\) (see Section 3.1 step 3), can be expressed as [3]
\[
\hat{R} = H\hat{X}
\]
where \(\hat{H}\) is the user’s equivalent frequency domain channel matrix of size \(M \times M\). The vector \(\hat{X}\) is frequency domain representation of soft decision \(\hat{X}\), which is computed from the knowledge of the a prior LLRs provided by the SISO decoder at the previous iteration, given by \(\hat{X} = \hat{F}_d\hat{x}\) , \(\hat{x} = [\hat{x}_0, \hat{x}_1, ..., \hat{x}_{M-1}]\) . Under the assumption of independent modulated bits because of the exist of the bit interleaver, we have [11]
\[
\bar{x}_s = E\{x_s\} = \sum_{j=1}^{2^N} \alpha_j P(x_s = \alpha_j)
\]
\[
\sigma_x^2 = E\{(x_s - \bar{x}_s)^2\} = \sum_{j=1}^{2^N} (\alpha_j - \bar{x}_s)^2 P(x_s = \alpha_j)
\]
where \(\alpha_j \in \mathbb{S}\) , \(P(x_s = \alpha_j) = \prod_{j=1}^{N} P(c_{s,j} = h_j)\).

The frequency domain error vector between the equalized signal vector \(\hat{R}\) and the true signal vector \(X\) is given by
\[ \Delta = \hat{R} - \hat{X} = W\hat{R} - B\hat{X} - X \]
\[ = W\hat{H}X - B\hat{X} - X \]

The MSE is
\[ \text{MSE} = \text{tr} \left\{ E \left\{ \Delta \alpha \right\} \right\} = \text{tr} \left\{ W \left( \sigma_g^2 \| \hat{H} \| + \sigma_n^2 \right) W^H \right\} \\
- \text{tr} \left\{ \sigma_g^2 \left( WHB^H + B\hat{H}^H W^H \right) \right\} \\
+ \text{tr} \left\{ - \sigma_n^2 \left( W\hat{H} + \hat{H}^H W^H \right) + B\sigma_n^2 B^H \right\} \\
+ \text{tr} \left\{ \sigma_n^2 \left( B + B^H \right) + \sigma_g^2 \right\} \]

To avoid self-subtraction of the desired symbol by its previous estimate, the MSE is minimized with respect to \( W \) and \( B \) subject to the constraint \[ \text{tr} \left\{ B \right\} = 0 \]

We use Lagrange multiplier method to solve the minimum of MSE. Construct the cost function \( f(W, B, \lambda) \)
\[ f(W, B, \lambda) = \text{tr} \left\{ W \left( \sigma_g^2 \| \hat{H} \| + \sigma_n^2 \right) W^H \right\} \\
- \text{tr} \left\{ \sigma_g^2 \left( WHB^H + B\hat{H}^H W^H \right) \right\} \\
+ \text{tr} \left\{ - \sigma_n^2 \left( W\hat{H} + \hat{H}^H W^H \right) + B\sigma_n^2 B^H \right\} \\
+ \text{tr} \left\{ \sigma_n^2 \left( B + B^H \right) + \sigma_g^2 \right\} + \lambda \text{tr} \left\{ B \right\} \]

where \( \lambda \) is the Lagrange multiplier. By setting the gradient of (14) with respect to \( W \), \( B \) and \( \lambda \) to zero, respectively. We obtain
\[ \frac{\partial f(W, B, \lambda)}{\partial W} = \left( \sigma_g^2 \| \hat{H} \| + \sigma_n^2 \right) W \\
- \sigma_g^2 B\hat{H}^H - \sigma_n^2 \hat{H}^H = 0 \]
\[ \frac{\partial f(W, B, \lambda)}{\partial B} = W\hat{H} - \left( 1 + \frac{\lambda}{2\sigma_n^2} \right) I = 0 \]
\[ \frac{\partial f(W, B, \lambda)}{\partial \lambda} = \text{tr} \left\{ B \right\} = 0 \]

From (15), (16) and (17), we can get
\[ \Gamma = \left( 1 - \frac{\sigma_n^2}{\sigma_g^2} \right) \| \hat{H} \|^2 + \frac{\sigma_n^2}{\sigma_g^2} \]
\[ \kappa = \frac{1}{N} \sum_{n=0}^{N-1} \| \hat{H}_n \|^2 \Gamma^{-1} \]
\[ \mu = \frac{\kappa}{1 + \frac{\sigma_n^2}{\sigma_g^2} \kappa} \]

3.3 Equivalent AWGN channel assumption

At the output of FD-SDFE, in order to demodulate the transmitted symbols, we assume that the estimate \( \hat{x} \) is the output of an equivalent AWGN channel having \( x \) as its input \[ \hat{x} = \mu x + \eta \]

where \( \mu \) is the equivalent amplitude of the signal at the output, \( \eta \) is a complex white Gaussian noise with zero mean and variance \( \delta_n^2 \). The parameters \( \mu \) and \( \delta_n^2 \) are calculated at each iteration as a function of FD-SDFE structure. The variance \( \delta_n^2 \) can be expressed as
\[ \delta_n^2 = \mu (1 - \mu) \sigma_g^2 \]

4. SIMULATION RESULTS

4.1 Performance simulation

In this section, the bit error rate (BER) performance of the proposed FD-SDFE turbo equalization scheme for SC-FDMA system is evaluated. We consider an coded SC-FDMA system with 5MHz system bandwidth, 512 subcarriers ( \( N = 512 \) ), the number of users \( U = 4 \), then \( M = 128 \) subcarriers can be allocated to each user with interleaver subcarrier mapping scheme, \( subband = 2 \). No CP ( \( G = 0 \) ), and a 1/2 -rate convolutional code with constraint of 3. The encoded binary bits are interleaved randomly and mapped to QPSK symbols. To illustrate the performance of SC-FDMA under practical system, the 6-path frequency-selective fading channel is generated based on the International Telecommunication Union (ITU) Vehicular A (ITU V-A) channel model [14] listed in Table 1. The channel decoder employs log-MAP algorithm. It is assumed that the channel information is not known at the transmitter, and perfect channel estimation is available at the receiver.

<table>
<thead>
<tr>
<th>Delay (ns)</th>
<th>Power (dB)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>1</td>
<td>-1.0</td>
</tr>
<tr>
<td>2</td>
<td>-9.0</td>
</tr>
<tr>
<td>3</td>
<td>-10.0</td>
</tr>
<tr>
<td>4</td>
<td>-15.0</td>
</tr>
<tr>
<td>5</td>
<td>-20.0</td>
</tr>
</tbody>
</table>

Fig. 3 shows the BER performance of the proposed FD-SDFE turbo equalization scheme as functions of signal-to-
noise ratio (SNR). In this figure, the performance curves of frequency domain approximate linear MMSE turbo equalization (FD-TEQ) [13], and high complexity maximum a posteriori probability (MAP) equalization [6] are also provided for comparison. The output of a non-iterative equalizer corresponds to the MMSE-based FD linear equalization (MMSE-FD-LE).

From Fig. 3, it is clear that the first iteration \( (I = 1) \) yields a significant performance improvement with respect to MMSE-FD-LE, for example, at BER \( = 10^{-3} \), the proposed FD-SDFE turbo equalization scheme achieves 2.7 dB gain, whereas the gain is 1.6 dB for FD-TEQ. At the same time, three iterations were sufficient for FD-TEQ to converge, because it cannot fully exploit the a priori information from SISO decoder. However, the proposed scheme can adaptively update the feedforward and feedback coefficients by taking advantage of the a priori information from SISO decoder, thus, as we can see, with two iterations, the performance is around 0.7 dB better than the first iteration, then the gain of further iterations tends to decrease. After four iterations, the proposed FD-SDFE converges, and is about 0.4 dB worse than the optimal MAP algorithm at BER \( = 10^{-3} \).

4.2 Complexity analysis

The complexity analysis is concentrated in equalization computation. We quantify the complexity in terms of the number of complex multiplications per block. Similarly to the frequency domain turbo equalization in [5], [9], the total of the proposed FD-SDFE is dominated by the DFT, and is shown to be of order \( \mathcal{O}(M \log M) \), which is only linear in the block length. Compared with MAP algorithm complexity \( \mathcal{O}(MQ^4) \), the proposed FD-SDFE significantly reduces computational complexity. At the same time, it avoids the calculation of the cross-correlation function between the detected symbols and the transmitted symbols in each iteration in [5], thus achieves the better tradeoff between the performance and computational complexity.

5. Conclusions

A MMSE-based frequency domain soft-decision feedback turbo equalization scheme has been proposed for SC-FDMA system with insufficient CP. The scheme first uses RISIC algorithm and soft-decision symbols to cancel IBI and ICI interference, then FD-SDFE equalization is performed to further suppress residual interference caused by frequency-selective fading channel. These two steps are iterative until desired receiver performance is achieved. Simulation results show that the FD-SDFE turbo equalization scheme can improve the BER performance without CP, thus increase spectral efficiency of SC-FDMA system.

References


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