Parallelization Research of Circle Detection Based on Hough Transform

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Abstract
There is a problem of too long computation time in Circle detection of Hough transform. In this paper, two paralleled methods are given based on Threading Building Blocks (TBB) and CUDA, by utilizing multi-core and GPU, the most time-consuming part of circle detection is coped with parallelization. Experimental results show that the circle detection algorithms proposed in this paper has extremely good result of acceleration.

Keywords: Hough Transform, CUDA, Circle Detection, Threading Building Blocks (TBB).

1. Introduction
Hough transform is an effective method of binary image detection on straight line, circle, ellipse and other graphics. Later, it was proposed that generalized Hough transform can detect arbitrary shape graphics. Hough transform has been applied not only to graphical boundary recognition identification, it also in biomedical, office document image processing, SAR/ the ISAR image processing and automatic interpretation of aerial images [1-2].

Traditional Hough transform can achieve very good results in the case of relatively simple image detection, but there is the drawback of long computing time and the large cost of memory space, which has been proposed many kinds of efficient solutions. Hough transform in a variety of computing models are suggested in the reference[3-15], reference[16-19] propose a variety parallel Hough transform algorithm based on special processor or programmable logic hardware. Although these methods can improve Hough transform efficiency, the higher hardware requirements make these methods inaccessible under normal circumstances. Paper [20] gives a Hough transform line detection method based on GPU, but the acceleration rate is less than 3 times. Paper [21] gives a multi-core parallelization method; the acceleration effect is not significantly obvious with paper [20].

In this paper, Hough transform circle detection parallel methods based on Threading Building Blocks (TBB) and CUDA are given and higher acceleration is achieved.

2. Sequential Algorithm of Circle Detection
Circular contour detection plays a very important role in the field of image pattern recognition [22]. In the Circle Hough Transform (CHT), the center and the radius of the circle are two basic parameters; CHT on the circle extraction is to use the accumulation of three dimensional parameter [23-24].

Under normal circumstances, the circle in the image space after Hough transform, which is mapped onto the parameter space, is three-dimensional; therefore, it is needed to establish a three-dimensional accumulation array A (a, b, r) in the parameter space for each edge point in the image to accumulate after the calculation. The algorithm steps are as follows:

1) The image edge detection and binarization.
2) To calculate the minimum and maximum values of parameters a, b, r according to the size of the image, and to establish three-dimensional discrete parameter space, the size of the parameter space is determined by the minimum and maximum values of a, b, r.
3) To create an accumulator array A (a, b, r) in the parameter space, and each element set to 0.
4)To act Hough transform on the boundary point of the image, that is to calculate the corresponding curve of the point (a, b, r) in three-dimensional grid by the formula (1), and the corresponding accumulator adds 1, namely,

\[ A(a, b, r) = A(a, b, r) + 1 \] (1)

5) To find out the local maxima value of the corresponding image circle point accumulator, the value of the radius of the circumference point on the image plane, and the center of the circle parameters.

This value provides the parameters of the radius and the center of the circle on the image plane.
Because of discretization of the image, and in order to reduce errors, the formula (1) can be rewritten into the formula (2) in the actual calculations.

\[
(a_0 - x)^2 + (b_0 - y)^2 - r^2 \leq \xi
\]

(2)

Where \(\xi\) is the compensation of image quantization and digitization, \(r\) is set to be incremental variable in the calculation. Before every step iteration, \(r\) must be fixed, then to seek the point of the circumference of the center \((x_i, y_i)\), these points are in the plane \((a, b)\), which is perpendicular to \(r\), and to accumulate points of the track and corresponding points of the three-dimensional accumulation array \(A(a, b, r)\) of the plane mapping. \(r\) progressively increases from 0 to the upper limit of the image plane, increment of \(r\) each time corresponds to a flat image, for the point \((x_i, y_i)\), \(a\) and \(b\) vary between \([0, 2\pi]\) [24].

When the Hough transform parameter space is less than two dimensional, the transformation can achieve the desirable results. However, once the parameter space is more than two-dimensional, it will need a lot of storage space and time, which makes the Hough transformation only in theory, can not be applied in practice. Only by reducing the dimension of the parameter space, can Hough transformation be achieved in practice. It is assumed that by a prior knowledge to determine the radius of the circle or the radius range [25], can effectively reduce the space and time overhead in the calculation process [24].

For the circle in the image, the center \((a, b)\), the gradient direction of the edge point \((x_i, y_i)\) is \(\theta\), and the gradient direction of each edge point points to the center of the circle. Easy to know the gradient information of the point is:

\[
\begin{align*}
  g &= \sqrt{g_x^2 + g_y^2} \\
  \theta &= \arctan\left(\frac{g_y}{g_x}\right)
\end{align*}
\]

Then the center of a circle formula can be expressed as the follow formula:

\[
\begin{align*}
  a &= x_i - r \cos \theta \\
  b &= y_i - r \sin \theta
\end{align*}
\]

Which \(\sin \theta\), \(\cos \theta\) can be directly expressed as: \(\sin \theta = g_y/g\), \(\cos \theta = g_x/g\).

Therefore, the local gradient information of edge pixels can reduce the computation [25], and can improve the accuracy of the center coordinates. First to detect edge of the original image, and to do gray value variation and gradient solution of the pixel, then to do gradient image binarization according to given gradient threshold. Recorded the pixel of the gradient amplitude is large than the threshold, and gradient direction of the pixel is set to 1, gradient direction of the pixel of the gradient amplitude is smaller than the threshold and is set to 0. Scan all nonzero pixels and along the gradient direction on the corresponding two-dimensional accumulation array unit to add 1, the local maximum value in the accumulation array is the center coordinates. The fact that this method makes the dimensional number of accumulation array in Hough transform circle detection algorithm is reduced to two-dimensional from the three-dimensional. And to change the traditional method of one-to-many mapping into one-to-one mapping, this method has been the default standard Hough transform circle detection method [24].

3. Parallel Circle Detection Method Based on TBB

TBB (Threading Building Blocks) is a set of C++ template library. It provides the appropriate abstractions, and more content. In parallel programming, such contents the task concept, automatic load balancing features, mature, achievement of commonly algorithm, flexibility and scalability of no bound to the number of CPUs and so on.

TBB programming uses a template as the usual parallel iterative model. TBB is a paralleled programming model for the program scalability can be improved, and fully supports the nested. It can run on Windows, MacX and Linux, and Support Intel C++, VC7 / and gcc compiler. In order to build a large-scale paralleled program, allowing programmers to create their own paralleled components [26].

3.1 Algorithm Implementation

According to the circle detection serial algorithm, the most time-consuming in the detection process is the fourth step of the algorithm, and this process is loop iteration, we can parallel the step of the algorithm. In this loop iteration, every iteration is independent from each other, so we can make loop parallel. Parallel reduction of TBB template classes can be achieved. Because cycle accumulation in the program is reentrant, previous running results will not affect next results, so the template class paralleled for can be used.

For the loop parallelization, the first is the loop body converted into the form of a small space to operate.
The form is function object of the standard template library-style, known as the body object, an operator () deals with a little space. Note the iteration spatial parameter of the method operator(), blocked range $<T>$ is a template class defined in the library, which indicates the one-dimensional iteration space on the type $T$. Iteration space of other types in class parallel for can also be used. Defined blocked_range 2d in the library is used to represent the two-dimensional iteration space. Opfor need to include a number of member variables to save the local variables defined outside the loop, but used inside the loop. Generally, these member variables are initialized in the constructor of body object, but parallel for does not need to care about the body object is how to be created. Template function parallel for requires that the body object has a constructor copy. This constructor is used to create a separate copy for each working thread, and the parallel for will call destructor to destroy those copies.

In most cases, the implicitly generated copy of the constructor and destructor can work correctly. If the design requires the destructor to perform some other operations, such as free memory, then you need to define an explicit destructor. If copies of the constructor and destructor are generated by default, then it can not be achieved these additional operations. However, if the destructor is explicit, the constructor copies usually need to be explicit. As the body object may be copied, therefore, it should not be modified in its operator () method, so parallel _for will declare operator () method of the body object as const.

3.2 Experimental Results and Analysis

Experiment has been performed on one platform with sequential program and parallel program so as to testify the efficiency of parallel algorithm. The specific environment is given below.


Hardware platform: Intel Core2 Quad Q9400 2.66GHz, memory of DDR3 4G.

Table 1 and figure 1 show the experimental results which gives the speed-up for different problems. Picture 1 is an office picture; Picture 2 is a personal photo, it contains less amount of information; Picture 3 is 1/16 of 292M China map from Internet; Picture 4 is 1/8 of China map; Picture 5 is 1/4 of China map; Picture 6 is 1/2 of China map. The amount of information contained in the China map is relatively large, sequential processing required for a long time, Execution time is given in seconds.

<table>
<thead>
<tr>
<th>Example</th>
<th>Image size (M)</th>
<th>Image width and height (pixels)</th>
<th>Serial time (S)</th>
<th>Parallel time (S)</th>
<th>Speed-up</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>1.48</td>
<td>868*600</td>
<td>0.468</td>
<td>0.180</td>
<td>2.600</td>
</tr>
<tr>
<td>2</td>
<td>34.3</td>
<td>4000*3000</td>
<td>3.781</td>
<td>1.461</td>
<td>2.588</td>
</tr>
<tr>
<td>3</td>
<td>18.1</td>
<td>2772*2286</td>
<td>20.204</td>
<td>6.602</td>
<td>3.060</td>
</tr>
<tr>
<td>4</td>
<td>35.8</td>
<td>2795*4481</td>
<td>85.422</td>
<td>24.562</td>
<td>3.478</td>
</tr>
<tr>
<td>5</td>
<td>60.1</td>
<td>5973*4481</td>
<td>285.093</td>
<td>80.344</td>
<td>3.548</td>
</tr>
<tr>
<td>6</td>
<td>152</td>
<td>11922*4481</td>
<td>876.016</td>
<td>240.156</td>
<td>3.648</td>
</tr>
</tbody>
</table>

Based on the table 1 experimental results, some phenomena can be concluded.

1) Although the image size and the number of pixels of the picture 2 are larger than the picture 3’s, the selected picture is relatively simple, and round number is much less than the picture 3’s, so the execution time is less than the picture 3.

2) Though TBB task scheduling mechanism, the program is mapped to the four physical threads, the speed can, in theory, be increased by four times. However, due to communication synchronization and thread overhead consumption, and the program still exists the part of the serial procedures and other system overhead, Lead to the improvement program running speed will not reach the theoretical value.
3) Start multiple threads, when the scale of the problem is smaller, switching between threads frequent and initializing multiple threads will take up more time, so the speedup is not very large. With the expansion of the scale of the problem, the CPU usage is also growing, and multi-core advantage is gradually reflected. The proportion of time to start multiple threads in the program and the total execution time is getting smaller and smaller, so the speedup increasing.

4. Parallel Circle Detection Method Based on CUDA

4.1 Algorithm Principle

For the most time-consuming part of circle detection of sequential program, recorded the pixel of the gradient amplitude is larger than the threshold, and gradient direction of the pixel is set to 1, gradient direction of the pixel of the gradient amplitude smaller than the threshold is set to 0. Scan all non-zero pixels and along the gradient direction on the corresponding two-dimensional accumulation array unit to add 1, the local maximum value in the accumulation array is the center coordinates. This part can do parallel processing on the GPU, The steps of the algorithm are as follows:

Step1: open space and pass data to memory for the graphics card.

//Open space
CUDA_SAFE_CALL
(cudaMalloc((void**)&d_edges_data_ptr,
sizeof(uchar)*edges->height *edges->width));

// pass data to memory for the graphics card
CUDA_SAFE_CALL
(cudamempy(d_edges_data_ptr,edges_data_ptr,
sizeof(uchar)*edges->height*edges->width,cudaMemcpyHostToDevice));

Step 2: set the thread number and the number of blocks:
int THREAD_NUM = 512;
int blockNum = (width*height)/THREADS_NUM + ((width*height)%THREADS_NUM==0?0:1);

Step 3: calling kernel functions Hough2,
function prototype is as follows:

int min_radius, int max_radius,int acols, int arows, int* adata, int astep)
Step 4: copy the execution result of the kernel function on the graphics card back to memory
CUDA_SAFE_CALL(cudamempy(adata,d_adata,
sizeof(int)*accum->height*accum->width,cudaMemcpyDeviceToHost));;
Step 5: free memory space. Such as:
CUDA_SAFE_CALL(cudafreet(d_edges_data_ptr));

4.2 Experimental Results and Analysis

Experiment has been performed on one platform with sequential program and parallel program so as to testify the efficiency of parallel algorithm. The specific environment is given below.


Hardware platform: Intel Core2 Quad Q9400 2.66GHz, memory of DDR3 4G, NVIDIA GeForce GTX 260, 896 MB graphics memory,192 processing cores, 16KB shared memory, 8192 available registers in each Block, 32 warps, the maximum number of threads each block is 512.

Table 2 and figure 2 show the experimental results which gives the speed-up for different problems. The experimental pictures are the same picture with TBB experimental pictures. Execution time is given in seconds.

<table>
<thead>
<tr>
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<td>0.468</td>
<td>0.818</td>
<td>0.572</td>
</tr>
<tr>
<td>2</td>
<td>34.3</td>
<td>4000*3000</td>
<td>3.781</td>
<td>0.710</td>
<td>5.325</td>
</tr>
<tr>
<td>3</td>
<td>18.1</td>
<td>2772*2286</td>
<td>20.20</td>
<td>2.317</td>
<td>8.720</td>
</tr>
<tr>
<td>4</td>
<td>35.8</td>
<td>2795*4481</td>
<td>85.42</td>
<td>4.154</td>
<td>20.564</td>
</tr>
<tr>
<td>5</td>
<td>60.1</td>
<td>5973*4481</td>
<td>285.0</td>
<td>6.236</td>
<td>45.717</td>
</tr>
</tbody>
</table>
Based on table 2 experimental results, some phenomena can be concluded. With the increase in the amount of data, the parallel algorithm speedup on GPU increasing, however, the table shows two strange phenomenon: 1) First sequential program execution time is faster than parallel program; 2) Picture 2 sequential program execution time is slower than picture 1, parallel execution time is faster than the picture 1. The reason of the phenomenon 1: When the amount of data is small, Telecommunication transmission time of GPU and CPU is greater than the shortened time of the program executed on GPU.

The reason of the phenomenon 2: sequential program detects picture 2, the time consumption in CUDA parallel part, once this part uses the CUDA parallel execution, execution time is significantly shortened. While the parallel execution time of detecting Picture 1 is greater than the sequential detection time.

### 4. Conclusions

There is the problem of long computation time of Hough transform, This paper presents two parallel algorithm based on multi-core and GPU, Experimental result shows the maximum speedup can be achieved 3.648x based on 4-core system, The Maximum speedup can be achieved 45.7x based on GPU. Experiment results demonstrate the feasibility and efficiency of the parallel algorithms.

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### References


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